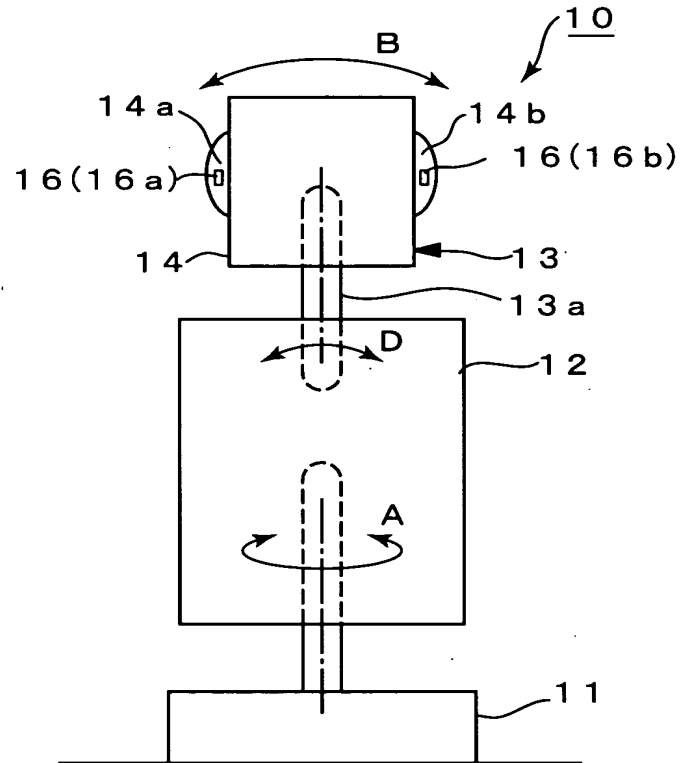


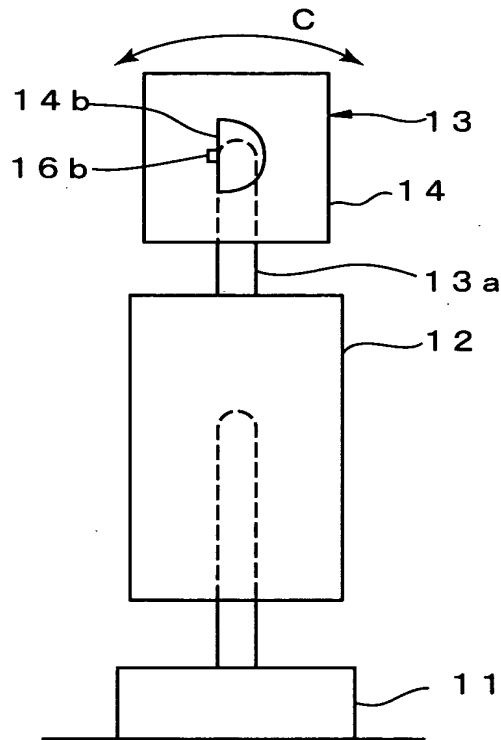
10/539047

FIG. 1



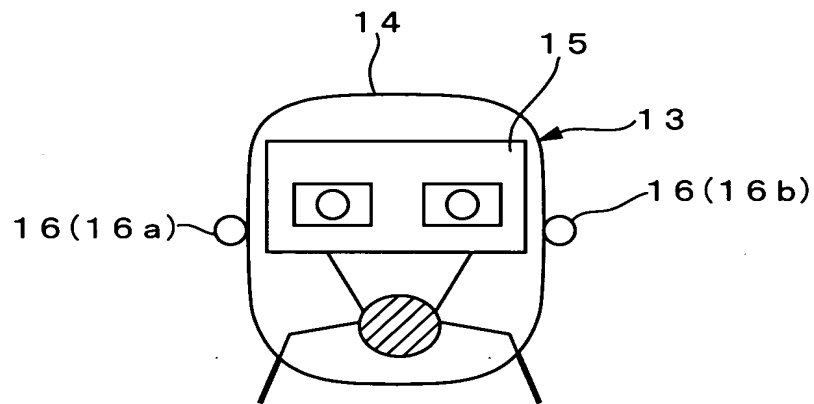
10/539047

FIG. 2



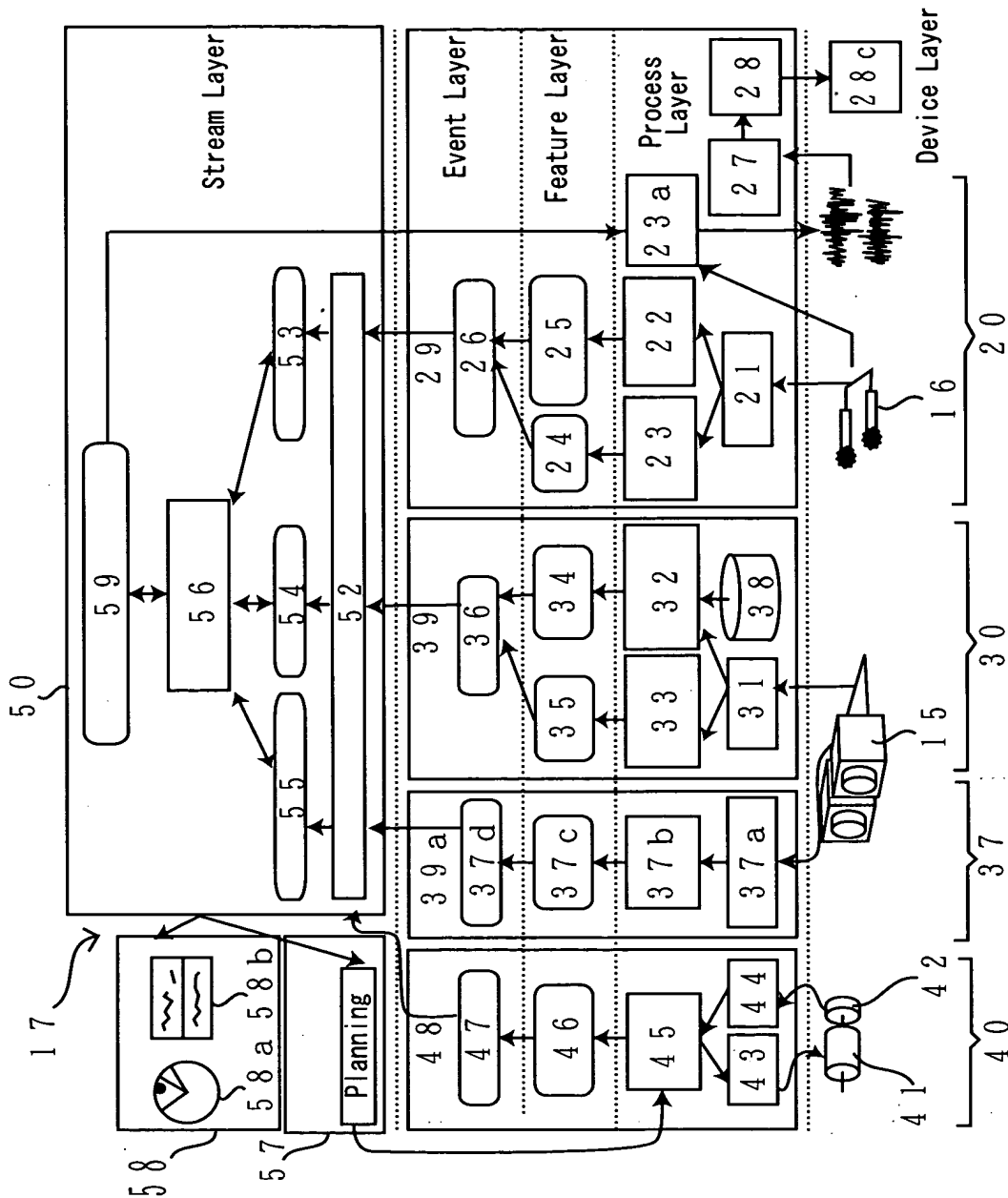
10/539047

FIG. 3



10/539047

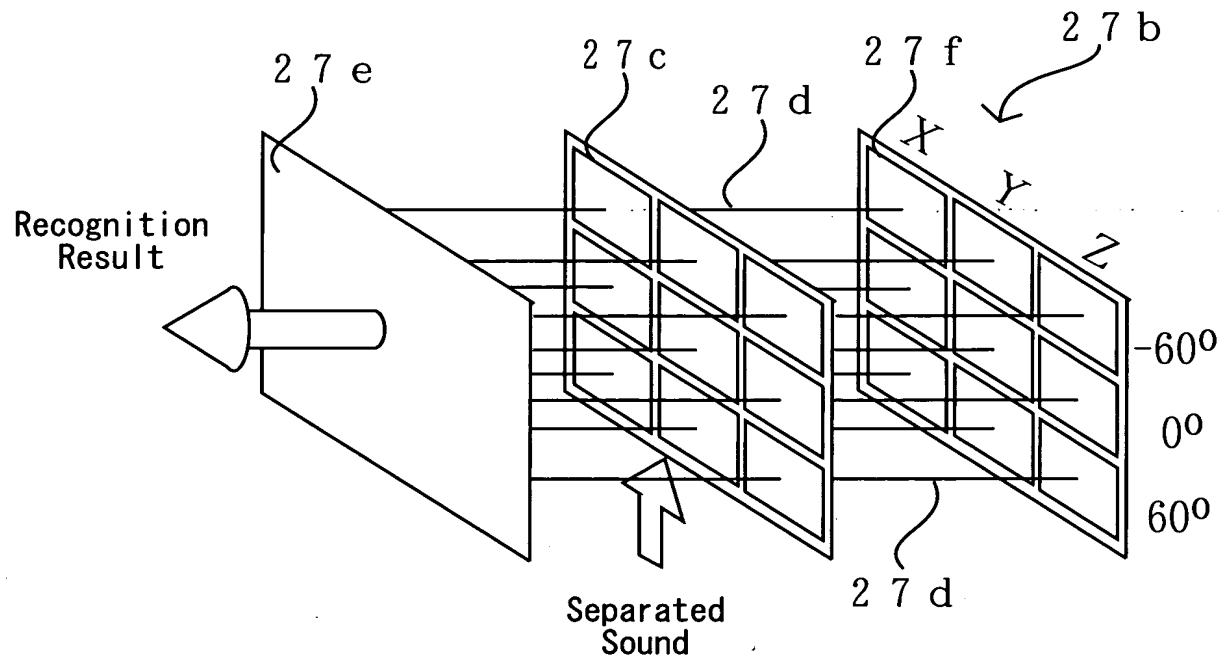
FIG. 4



The diagram illustrates a beamforming system 10. It begins with an antenna array 14 receiving signals from multiple sources 15. These signals pass through a pre-processor 16 and are distributed to several parallel processing channels labeled X1 through X9. Each channel performs specific operations such as beamforming, delay, and phase shifting. The outputs of these channels are combined in block X10, which displays a grid of sub-band signals. Finally, block X11 shows the resulting waveforms. The processed signals are transmitted via a cable 27 to a receiver unit 28.

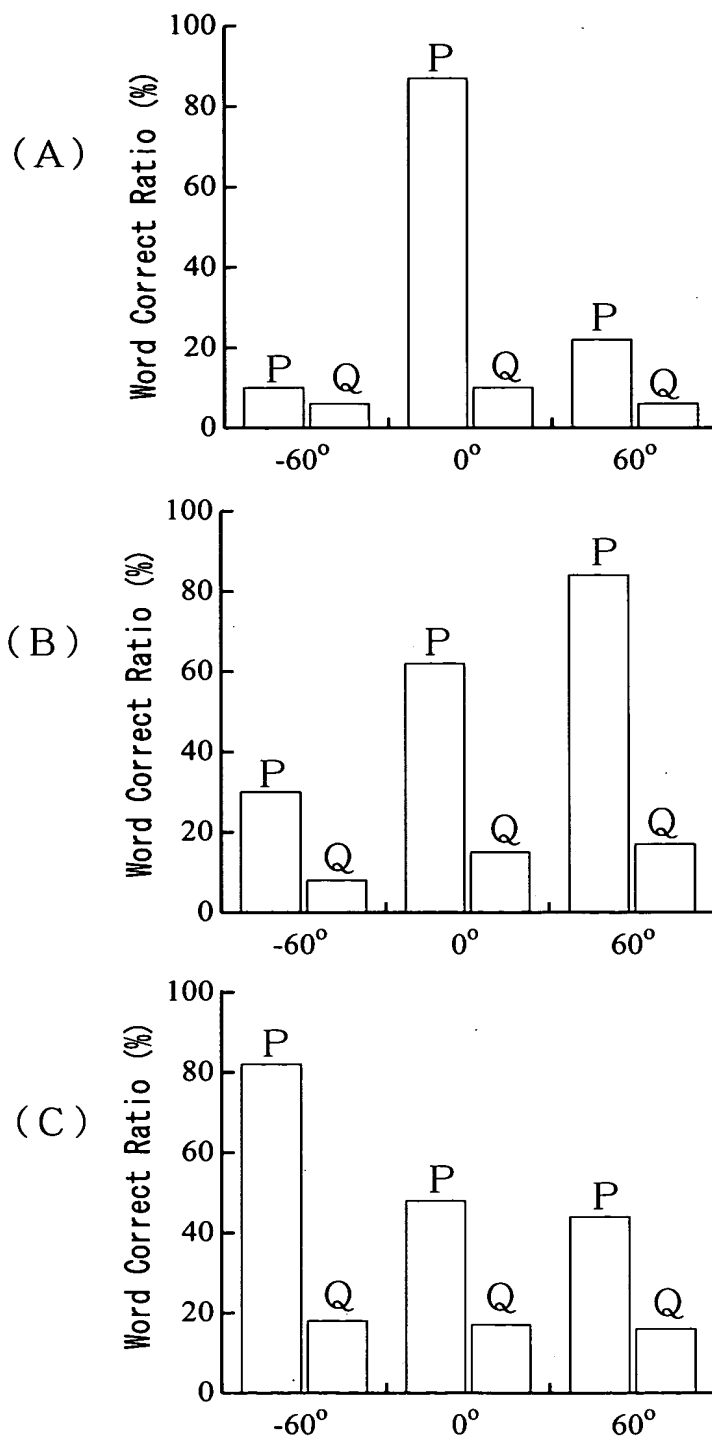
10/539047

FIG. 6



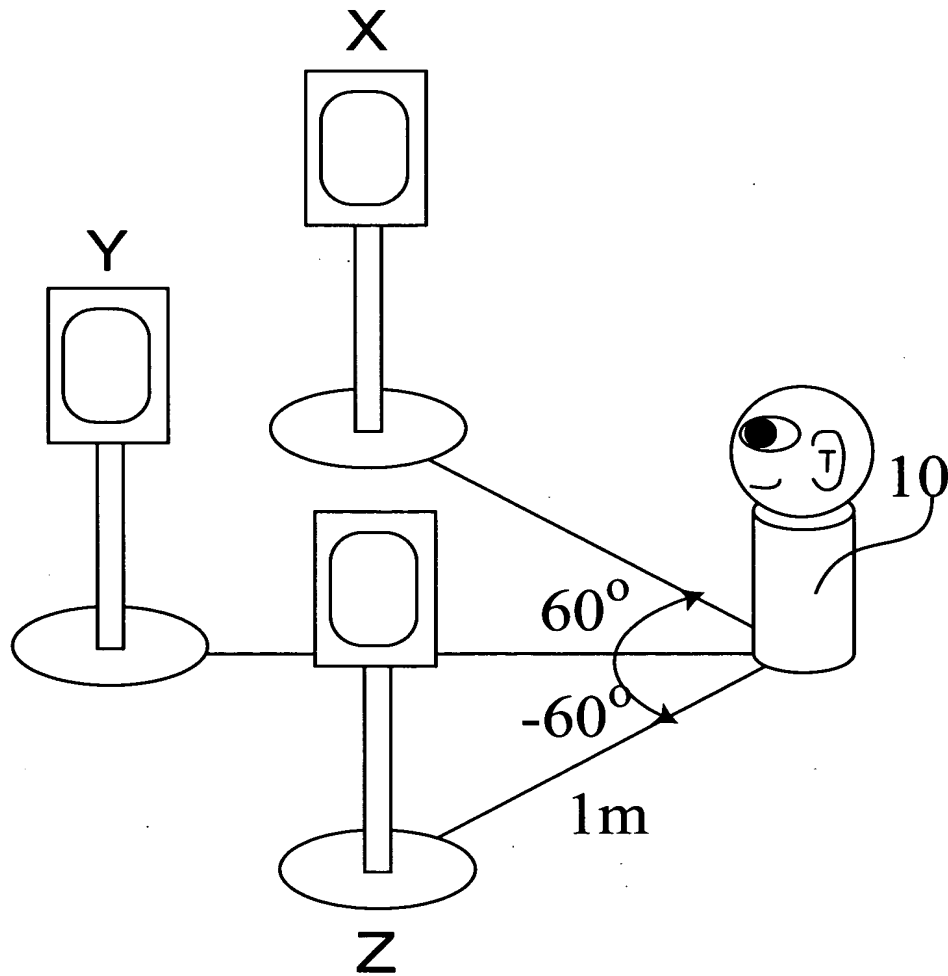
10/539047

FIG. 7



10/539047

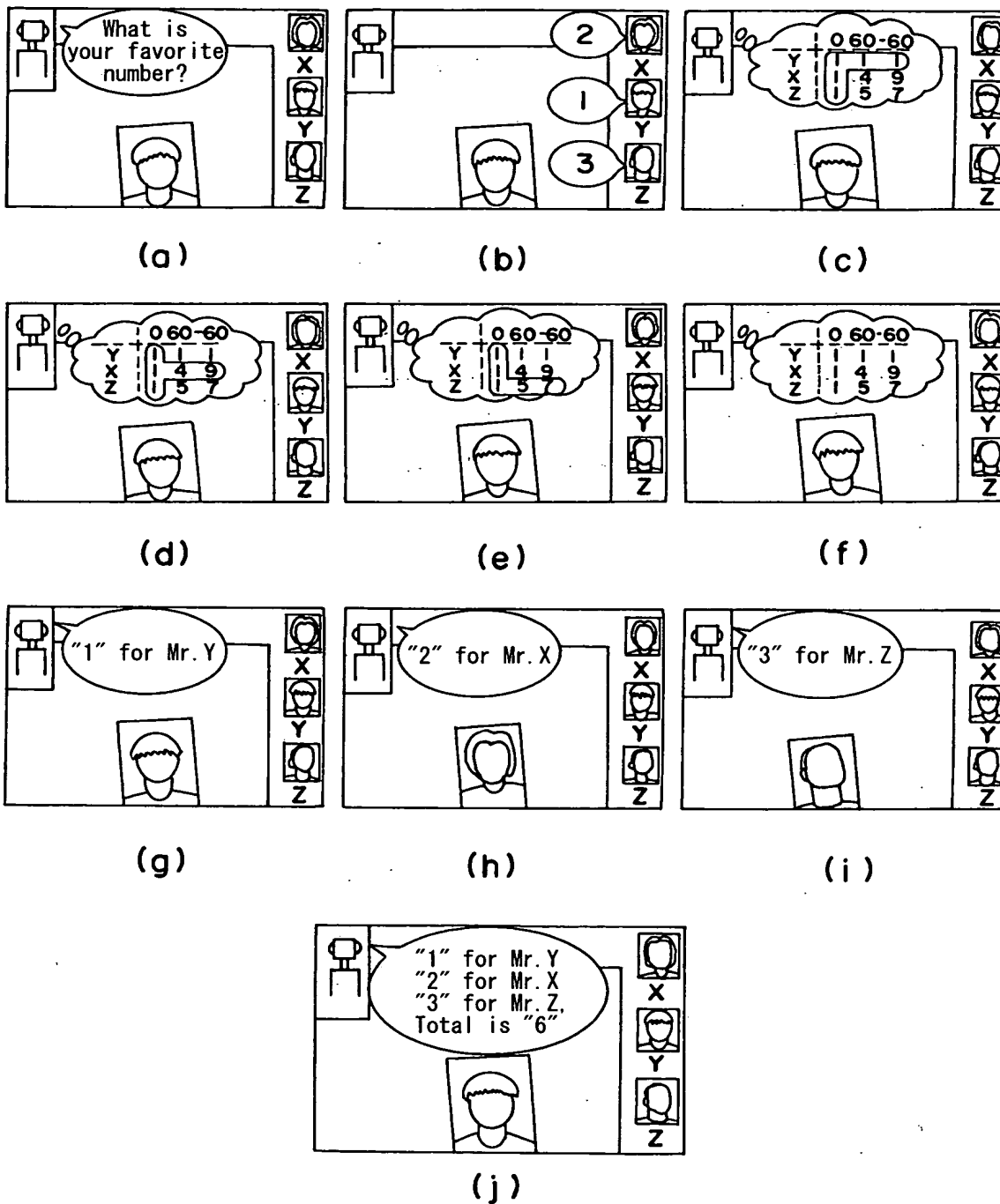
FIG. 8





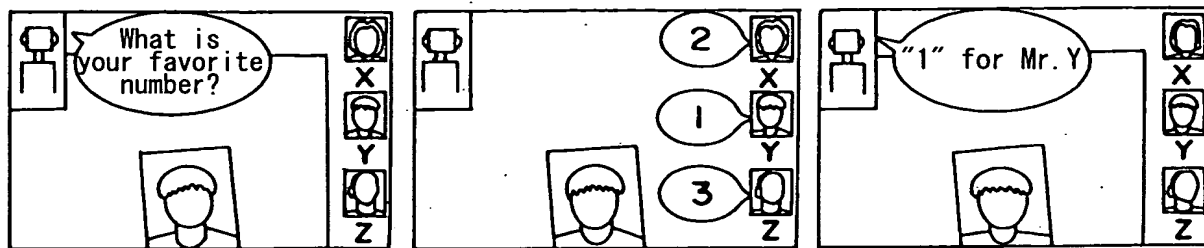
10/539047

FIG. 9

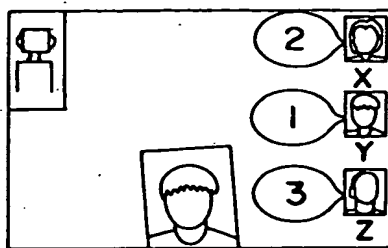


10/539047

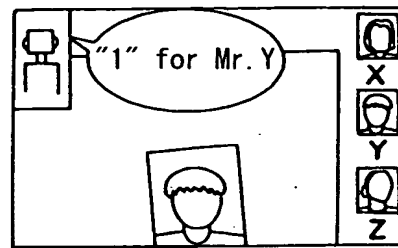
FIG. 10



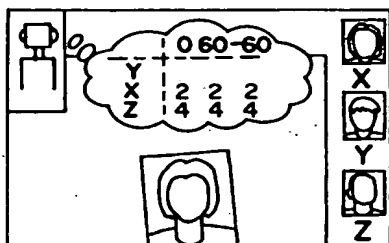
(a)



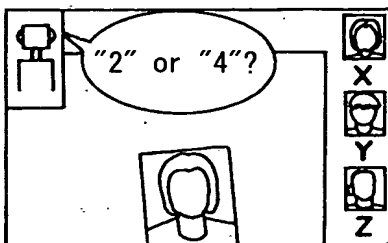
(b)



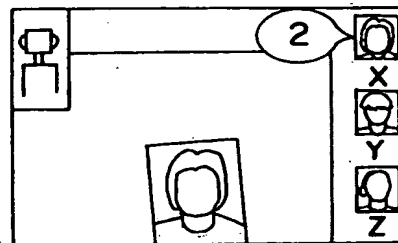
(c)



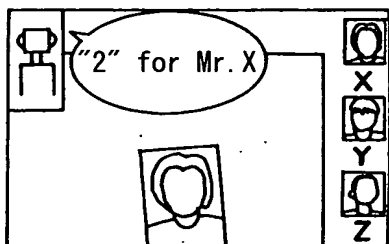
(d)



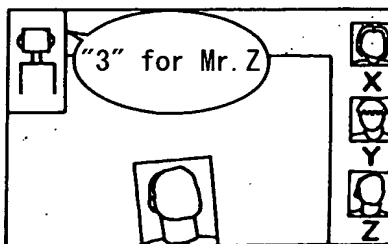
(e)



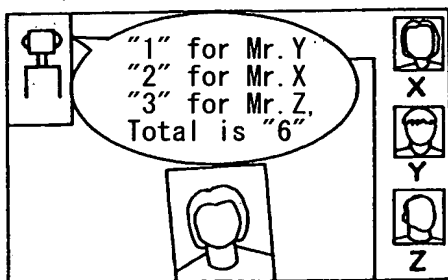
(f)



(g)



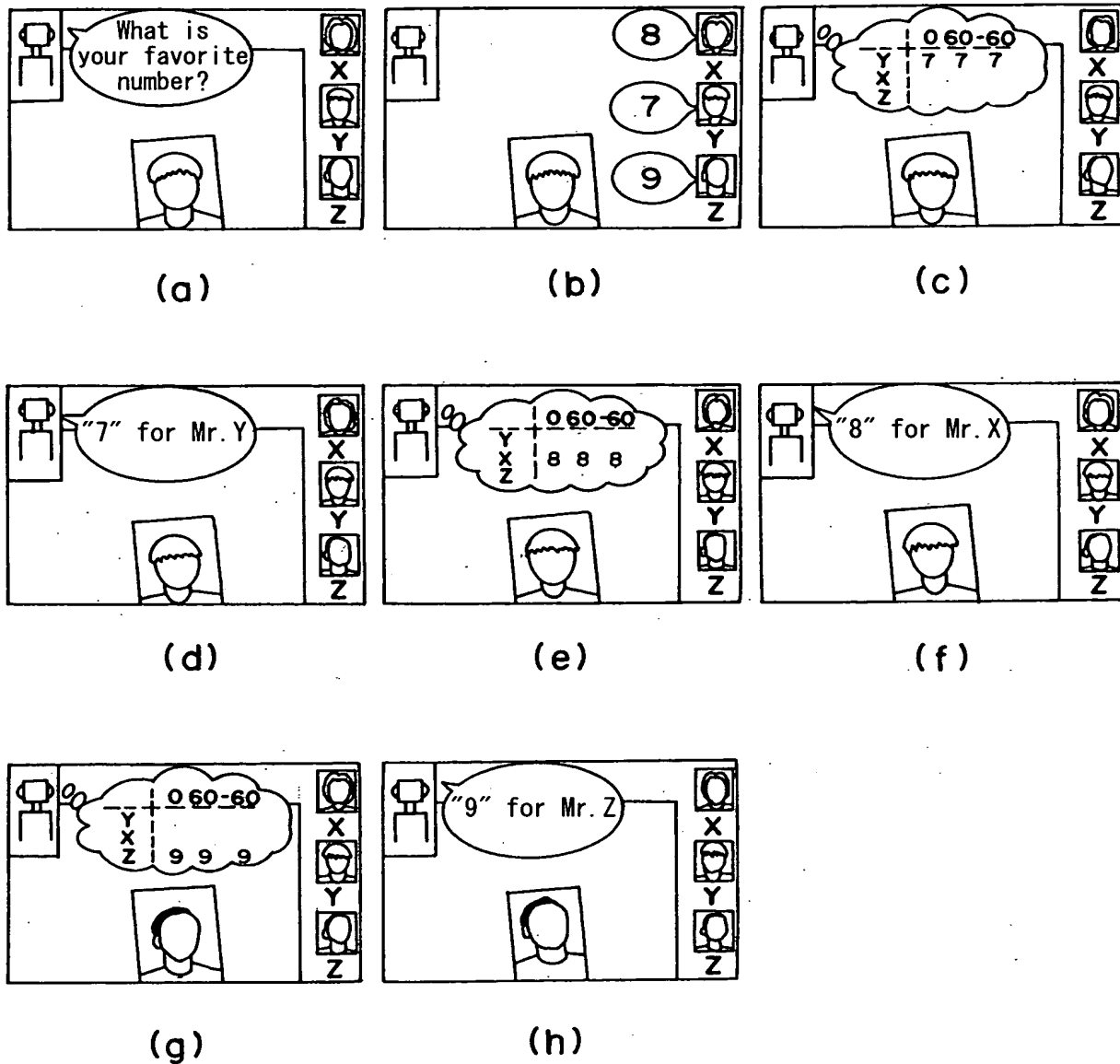
(h)



(i)

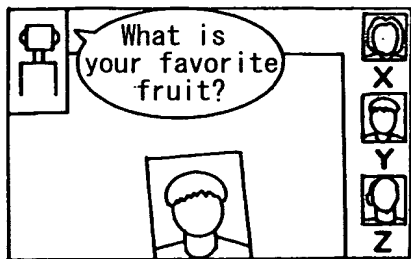
10/539047

FIG. 11

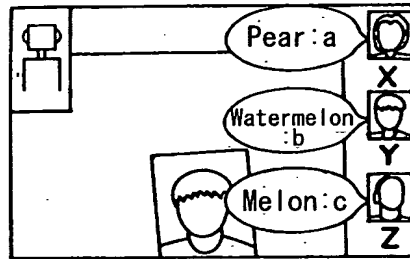


10/539047

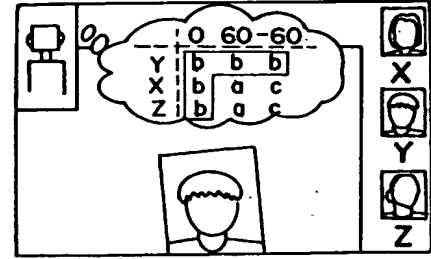
FIG. 12



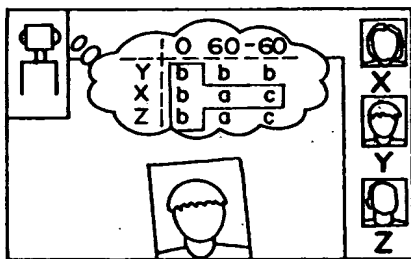
(a)



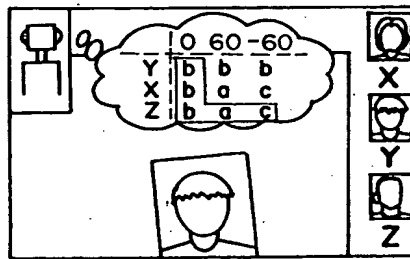
(b)



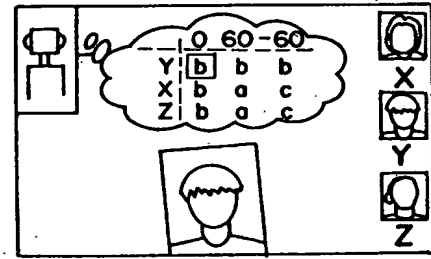
(c)



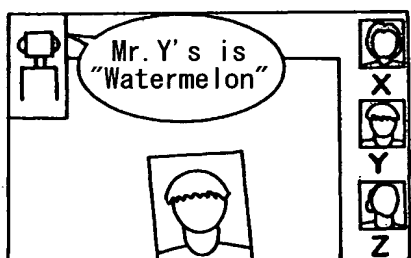
(d)



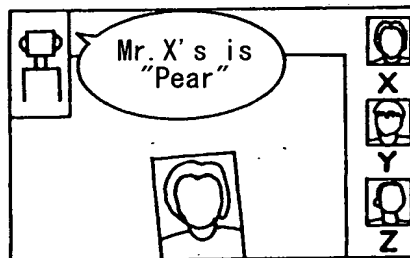
(e)



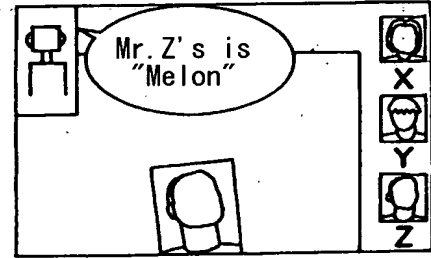
(f)



(g)



(h)



(i)

10/539047

FIG. 13

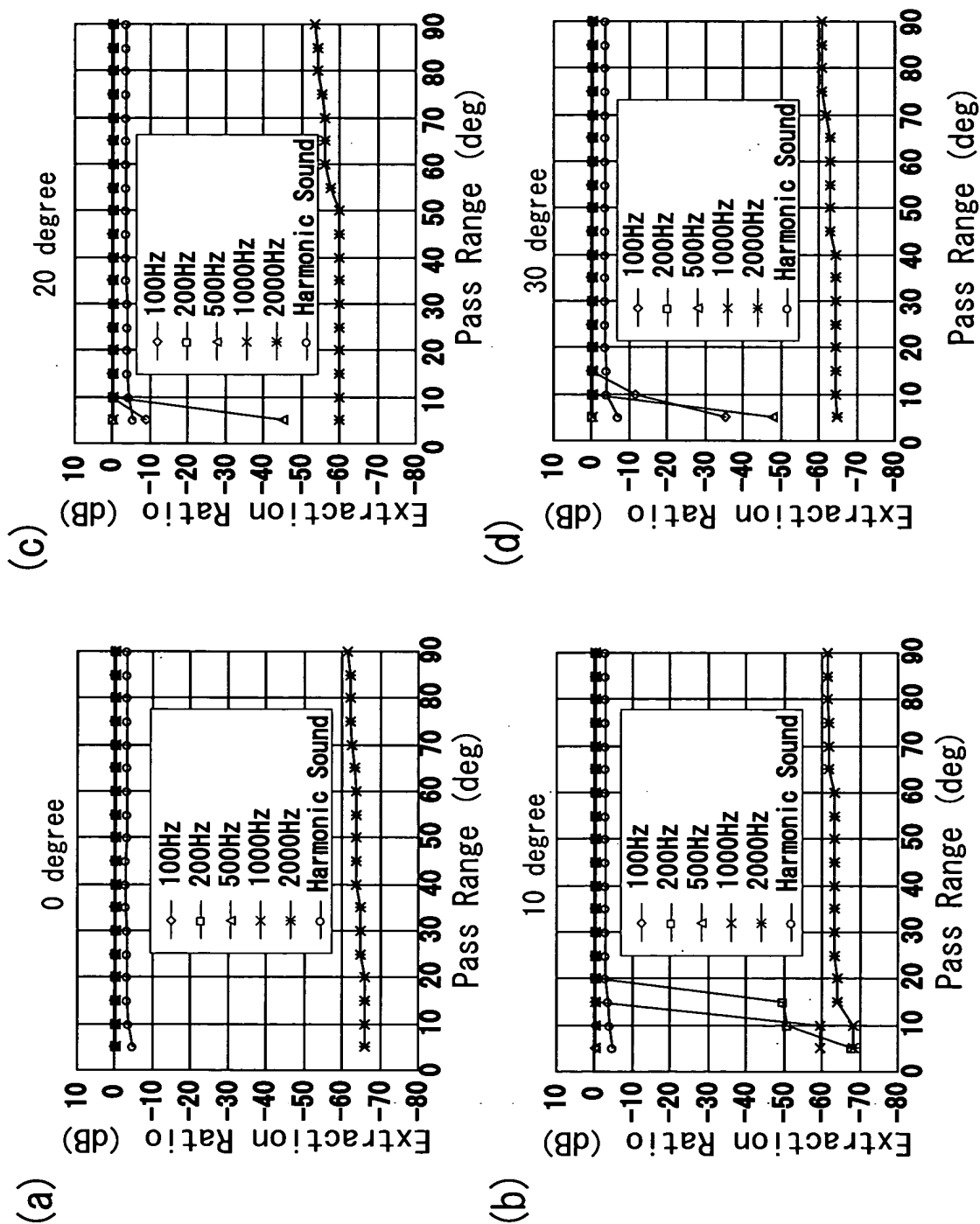
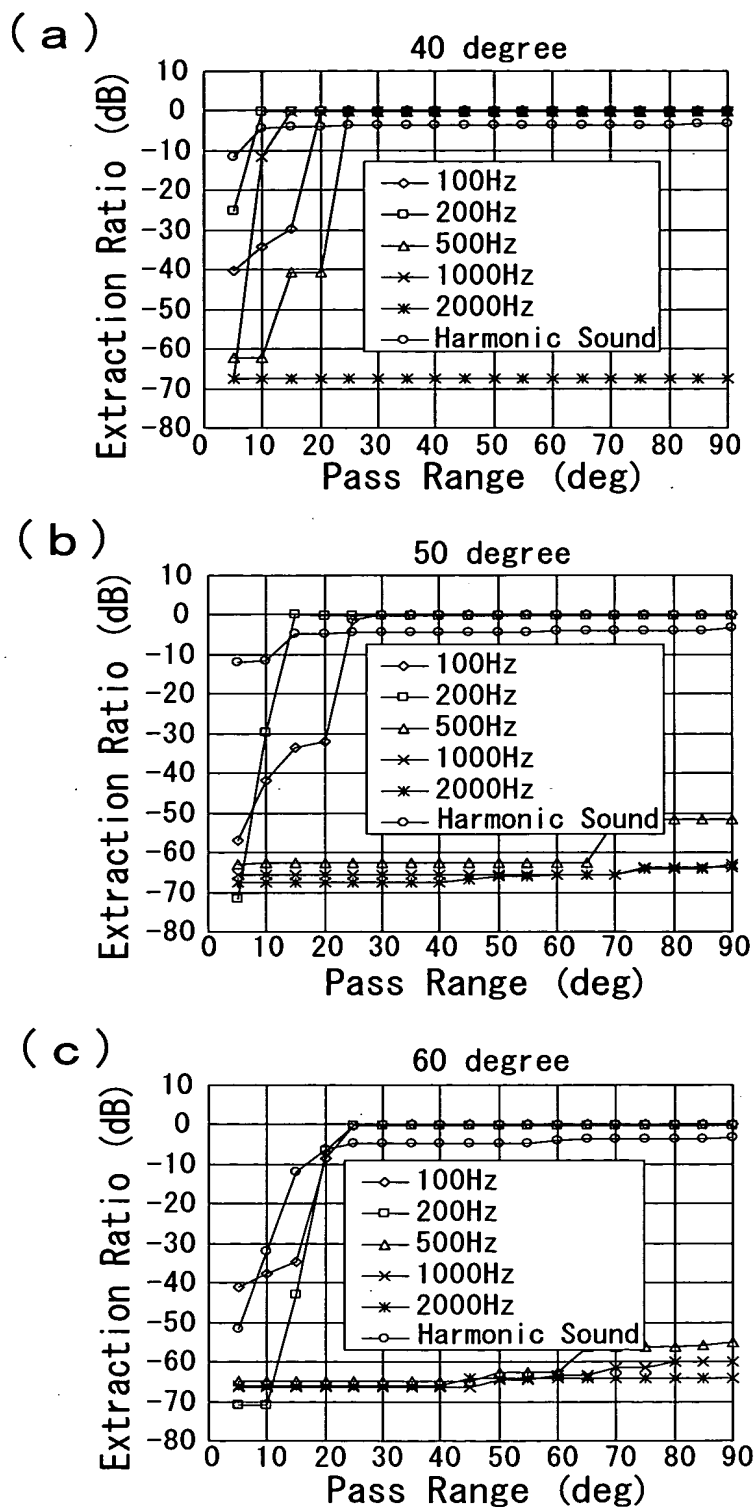


FIG. 14



10/539047

FIG. 15

